OIPE IAP3
MAY 01 2006
PATENT & TRADEMARK OFFICE

Docket No.: I4303.0068
(PATENT)

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re Patent Application of:
Klaus J. Oberlaender

Application No.: 10/777,710 | Confirmation No.: 5914

Filed: February 11, 2004 | Art Unit: 2186

For: FAST UNALIGNED CACHE ACCESS SYSTEM AND METHOD | Examiner: T. V. Thai

**SUBMISSION OF REVOCATION OF POWER OF ATTORNEY WITH NEW POWER OF ATTORNEY AND CHANGE OF CORRESPONDENCE ADDRESS, AND STATEMENT UNDER 37 CFR 3.73(B)**

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

Submitted herewith are (1) Revocation of Power of Attorney with New Power of Attorney and Change of Correspondence Address, and (2) Statement Under 37 CFR 3.73(b). Entry of these documents is respectfully requested.

Dated: April 28, 2006

Respectfully submitted,

By _____
Michael J. Scheer
Registration No.: 34,425
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PTO/SB/96 (12-05)
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# STATEMENT UNDER 37 CFR 3.73(b)

Applicant/Patent Owner: INFINEON TECHNOLOGIES AG

Application No./Patent No./Control No.: 10/777,710 Filed/Issue Date: February 11, 2004

Entitled: FAST UNALIGNED CACHE ACCESS SYSTEM AND METHOD

INFINEON TECHNOLOGIES AG (Name of Assignee) , a Corporation (Type of Assignee, e.g., corporation, partnership, university, government agency, etc.)

states that it is:

1. [X] the assignee of the entire right, title, and interest; or

2. [ ] an assignee of less than the entire right, title and interest.
(The extent (by percentage) of its ownership interest is _____ %)

in the patent application/patent identified above by virtue of either:

A. [ ] An assignment from the inventor(s) of the patent application/patent identified above. The assignment was recorded in the United States Patent and Trademark Office at Reel _____, Frame _____, or a true copy of the original assignment is attached.

**OR**

B. [X] A chain of title from the inventor(s), of the patent application/patent identified above, to the current assignee as follows:

1. From: Oberlaender, Klaus J. To: Infineon Technologies North America Corp.
The document was recorded in the United States Patent and Trademark Office at
Reel 015096, Frame 0530, or for which a copy thereof is attached.

2. From: Infineon Technologies North America Corp. To: INFINEON TECHNOLOGIES AG
The document was recorded in the United States Patent and Trademark Office at
Reel 015225, Frame 0687, or for which a copy thereof is attached.

3. From: _____ To: _____
The document was recorded in the United States Patent and Trademark Office at
Reel _____, Frame _____, or for which a copy thereof is attached.

[ ] Additional documents in the chain of title are listed on a supplemental sheet.

**As required by 37 CFR 3.73(b)(1)(i), the documentary evidence of the chain of title from the original owner to the assignee was, or concurrently is being, submitted for recordation pursuant to 37 CFR 3.11.**
[NOTE: A separate copy (*i.e.*, a true copy of the original assignment document(s)) must be submitted to Assignment Division in accordance with 37 CFR Part 3, to record the assignment in the records of the USPTO. See MPEP 302.08]

The undersigned (whose title is supplied below) is authorized to act on behalf of the assignee.

| | |
|---|---|
| Signature | April 28, 2006<br>Date |
| Michael J. Scheer<br>Printed or Typed Name | 212-835-1400<br>Telephone Number |
| Authorized Signer for Assignee<br>Title | |

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# REVOCATION OF POWER OF ATTORNEY WITH NEW POWER OF ATTORNEY AND CHANGE OF CORRESPONDENCE ADDRESS

I hereby revoke all previous powers of attorney given in the below-identified applications and patents.

I hereby appoint the practitioners associated with Customer Number: 38881.

Please change the correspondence address for the below-identified applications and patents to the address associated with Customer Number: 38881.

I am the assignee of record of the entire interest (unless noted otherwise) in each of the below-identified applications and patents. Statements under 37 CFR 3.73(b) for each of the respective applications/patents are enclosed with the submitted copies of this document.

Authorization to file this Revocation of Power of Attorney with New Power of Attorney and Change of Correspondence Address form listing multiple application and patent numbers rather than the assignee, Infineon Technologies AG, signing a separate form for each application and patent was provided by Magdalen Y. Greenlief (tel.: 571-272-8850) of the Office of the Manual of Patent Examining Procedure. The original copy of this document is filed in Application No. 11/391,666, which was filed on March 27, 2006.

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| *10/868,502 | 6/14/2004 | KEYPAD IN TEXTILES WITH CAPACITIVE READ-OUT CIRCUIT | Jung, Stefan; Lauterbach, Christl |
| *11/078,202 | 3/10/2005 | CHIP CARD | Puschner, Frank; Proske, Reinhard; Stampka, Peter; Muller-Hipper, |

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| *11/130,552 | 5/16/2005 | CONTROLLING OUT OF ORDER EXECUTION PIPELINES USING PIPELINE SKEW PARAMETERS | Norden, Erik K.; Arnold, Roger D.; Ober, Robert E.; Hastie, Neil S. |
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| 09/565,649 | 5/5/2000 | PROGRAMMABLE DATA ARITHMETIC ARRAYS | Wasson, Stephen L. |
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| 09/751,777 | 12/29/2000 | A FAST INITIAL ACQUISITION/SEARCH METHODOLOGY FOR CDMA AND W-CDMA SYSTEMS | Medlock, Joel D. |
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| 10/491,070 | 10/18/2004 | ELECTRICAL CIRCUIT | Baradie, Sammy; Kaibel, Martin; Weisgerber, Andreas |
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| 10/769,286 | 1/30/2004 | SECURITY MEMORY CARD AND PRODUCTION METHOD | Boker, Thorsten |
| 10/773,114 | 2/4/2004 | MICROELECTROMECHANICAL DEVICE AND METHOD FOR PRODUCING IT | Bottner, Harald; Schubert, Axel; Nurnus, Joachim; Jaegle, Martin |
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| 10/781,359 | 2/17/2004 | IC-CHIP HAVING A PROTECTIVE STRUCTURE | Aumuller, Christian; Janke, Marcus |
| 10/789,373 | 2/27/2004 | DEVICE AND METHOD FOR CALCULATING A RESULT OF MODULAR EXPONENTIATION | Seifert, Jean-Pierre; Velten, Joachim |
| 10/789,423 | 2/27/2004 | SOLDER, MICROELECTROMECHANICAL COMPONENT AND DEVICE, AND A PROCESS FOR PRODUCING A COMPONENT OR DEVICE | Bottner, Harald; Schubert, Axel; Jaegle, Martin |
| 10/794,588 | 3/5/2004 | MONOLITHIC INTEGRATED CIRCUIT ARRANGEMENT | Schindler, Dr. Gunthe |
| 10/803,174 | 3/16/2004 | NONCONDUCTING SUBSTRATE, FORMING A STRIP OR A PANEL, ON WHICH A MULTIPLICITY OF CARRIER ELEMENTS ARE FORMED | Barchmann, Bernd; Heinemann, Erik; Heitzer, Josef; Puschner, Frank |
| 10/803,175 | 3/16/2004 | FLUORESCENCE BIOSENSOR CHIP AND FLUORESCENCE BIOSENSOR CHIP ARRANGEMENT | Schienle, Meinrad; Brederlow, Ralf; Hofmann, Franz; Jenkner, Martin; Luyken, Richard Johannes; Paulus, Christian; Schindler-Bauer, Petra; Thewes, Roland |
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| 10/821,803 | 4/8/2004 | BIOSENSOR CIRCUIT AND SENSOR ARRAY CONSISTING OF A PLURALITY OF SAID BIOSENSOR CIRCUITS AND BIOSENSOR ARRAY | Eversmann, Bjorn-Oliver; Jenkner, Martin; Paulus, Christian; Thewes, Roland |
| 10/825,625 | 4/15/2004 | METHOD AND APPARATUS FOR PROTECTING AN EXPONENTIATION CALCULATION BY MEANS OF THE CHINESE REMAINDER THEOREM (CRT) | Fischer, Wieland; Seifert, Jean-Pierre |
| 10/826,881 | 4/16/2004 | SENSOR ARRANGEMENT | Eversmann, Bjorn-Oliver; Paulus, Christian; Stromberg, Guido; Thewes, Roland |
| 10/827,913 | 4/19/2004 | METHOD AND APPARATUS FOR PROTECTING A CALCULATION IN A CRYPTOGRAPHIC ALGORITHM | Fischer, Wieland; Seifert, Jean-Pierre |
| 10/831,455 | 4/23/2004 | INTEGRATED CIRCUIT AND METHOD FOR FABRICATING AN INTEGRATED CIRCUIT | Mayer, Albrecht |
| 10/846,483 | 5/13/2004 | SEMICONDUCTOR CHIP WITH FIB PROTECTION | Ungar, Franz; Schmid, Gunter |
| 10/848,997 | 5/18/2004 | CONFIGURABLE EMBEDDED PROCESSOR | Oberlaender, Klaus J.; Haines, Ralph; Chesters, Eric; Behrens, Dirk |
| 10/854,928 | 5/26/2004 | INTEGRATED CIRCUIT AND METHOD FOR OPERATING THE INTEGRATED CIRCUIT | Sedlak, Holger; Kunemund, Thomas |

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| 10/857,616 | 5/27/2004 | INTEGRATED CIRCUIT AND METHOD FOR OPERATING THE INTEGRATED CIRCUIT | Kunemund, Thomas |
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| 10/857,635 | 5/28/2004 | CIRCUIT ELEMENT HAVING A FIRST LAYER OF AN ELECTRICALLY INSULATING SUBSTRATE MATERIAL AND METHOD FOR MANUFACTURING A CIRCUIT ELEMENT | Weber, Dr. Werner; Schmid, Dr. Gunter; Luyken, J Richard; Thewes, Dr. Roland; Seitz, Dr. Markus |
| 10/861,315 | 6/4/2004 | METHOD OF MAKING CONTACT WITH CONDUCTIVE FIBERS | Jung, Stefan; Lauterbach, Christl |
| 10/861,316 | 6/4/2004 | APPARATUS AND METHOD FOR CONNECTING AN ELECTRIC CONDUCTOR ELECTRICALLY TO AN ELECTRONIC COMPONENT | Jung, Stefan; Lauterbach, Christl |
| 10/864,822 | 6/8/2004 | DATA PROCESSING DEVICE | Eckstein, Gernot; Kunemund, Thomas; Sedlak, Holger |
| 10/866,070 | 6/10/2004 | CIRCUIT ARRANGEMENT AND DATA PROCESSING METHOD | Pockrandt, Wolfgang |
| 10/868,501 | 6/14/2004 | CONSTRUCTION AND CONNECTION TECHNIQUE IN TEXTILE STRUCTURES | Jung, Stefan; Lauterbach, Christl |
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| 10/871,867 | 6/18/2004 | INDUCTIVE COMPONENT | Tiebout, Marc |
| 10/874,687 | 6/22/2004 | DEVICE AND METHOD FOR ENCRYPTING DATA | Fischer, Wieland; Seifert, Jean-Pierre |
| 10/890,523 | 7/12/2004 | METHOD AND APPARATUS FOR PRODUCING A SHEET-LIKE STRUCTURE | Jung, Stefan; Lauterbach, Christl |
| 10/893,161 | 7/16/2004 | SHIFT DEVICE AND METHOD FOR SHIFTING | Gammel, Berndt; Klug, Franz; Kniffler, Oliver |
| 10/893,163 | 7/16/2004 | CALCULATING UNIT AND METHOD FOR PERFORMING AN ARITHMETIC OPERATION WITH ENCRYPTED OPERANDS | Gammel, Berndt; Klug, Franz; Kniffler, Oliver |
| 10/896,366 | 7/21/2004 | METHOD AND APPARATUS FOR INTEGRATION OF ELECTRONICS IN TEXTILES | Jung, Stefan; Lauterbach, Christl |
| 10/899,444 | 7/26/2004 | ADHESIONS OF STRUCTURES FORMED FROM MATERIALS OF POOR ADHESION | Ruttkowski, Eike; Hofmann, Franz; Eversmann, Bjorn-Oliver |
| 10/902,486 | 7/29/2004 | IC CHIP WITH NANOWIRES | Mio, Hannes; Kreupl, Franz |
| 10/911,224 | 8/4/2004 | COMPUTER SYSTEM FOR DATA PROCESSING AND METHOD FOR THE TRANSFER OF AN ARRAY SEGMENT OF AN AFFINE-INDEXED MULTI-DIMENSIONAL ARRAY REFERENCED IN A LOOP NEST FROM A FIRST MEMORY TO A SECOND MEMORY | Richter, Mathias; Sauermann, Mirko |
| 10/913,707 | 8/5/2004 | MEMORY CELL | Deppe, Joachim; Kleint, Christoph; Ludwig, Christoph |
| 10/913,956 | 8/6/2004 | KEY MANAGEMENT DEVICE AND METHOD FOR THE ENCRYPTED STORAGE OF DIGITAL DATA WORDS | Gammel, Berndt M.; Kunemund, Thomas; Sedlak, Holger |
| 10/915,258 | 8/9/2004 | USB-BASED PERIPHERAL DEVICE AND METHOD FOR STARTING UP THE USB-BASED PERIPHERAL DEVICE | Gotze, Ralf; Schneckenburger, Christian; Cinkler, Kalman |
| 10/916,308 | 8/11/2004 | ELECTRODEPOSITION DEVICE AND ELECTRODEPOSITION SYSTEM FOR COATING STRUCTURES WHICH HAVE ALREADY BEEN MADE | Muller-Hipper, Andreas; |

| Appln. No. | Filing Date | Title | Inventor(s) |
|---|---|---|---|
| | | CONDUCTIVE | Simmerlein-Erlbacher, Ewald; Karl, Andreas |
| 10/917,262 | 8/12/2004 | DEVICE AND METHOD FOR CALCULATING A RESULT OF A DIVISION | Fischer, Wieland |
| 10/917,554 | 8/12/2004 | PROCESS FOR PRODUCING A STRUCTURED METAL LAYER ON A SUBSTRATE BODY, AND SUBSTRATE BODY HAVING A STRUCTURED METAL LAYER | Gundlach, Harald; Muller-Hipper, Andreas; Simmerlein-Erlbacher, Ewald |
| 10/920,043 | 8/16/2004 | PROCESS FOR PRODUCING A MULTILAYER ARRANGEMENT HAVING A METAL LAYER | Ruttkowski, Eike; Ilicali, Gurkan; Luyken, R. Johannes; Hofmann, Franz; Alba, Manuela |
| 10/920,150 | 8/17/2004 | CONTROL SYSTEM AND METHOD FOR OPERATING A TRANSCEIVER | Stromberg, Guido; Sturm, Thomas |
| 10/922,791 | 8/19/2004 | METHOD AND APPARATUS FOR COMPUTER-AIDED CREATION OF A CLOCK TREE STRUCTURE FILE, A METHOD FOR COMPUTER-AIDED CREATION OF A LAYOUT FOR A SEMICONDUCTOR CIRCUIT, AND A COMPUTER-READABLE STORAGE MEDIA | Endres, Heinz; Zettler, Thomas |
| 10/923,198 | 8/20/2004 | CIRCUIT ARRANGEMENT HAVING A TRANSMITTER AND A RECEIVER | Dirscherl, Gerd; Janssen, Norbert |
| 10/925,882 | 8/23/2004 | DATA CARRIER CARD | Fischer, Jurgen; Palm, Herbert; Puschner, Frank; Willer, Josef |
| 10/925,903 | 8/23/2004 | PSEUDORANDOM NUMBER GENERATOR | Dirscherl, Gerd; Gammel, Berndt; Gottfert, Rainer |
| 10/926,838 | 8/25/2004 | FLOATING GATE MEMORY CELL, FLOATING GATE MEMORY ARRANGEMENT, CIRCUIT ARRANGEMENT AND METHOD FOR FABRICATING A FLOATING GATE MEMORY CELL | Graham, Andrew; Hoffmann, Franz |
| 10/928,646 | 8/27/2004 | METHOD FOR COMPARISON A TEST FINGERPRINT,WHICH IS IN THE FORM OF AN IMAGE STRIP SEQUENCE, WITH A STORED REFERENCE FINGERPRINT, AND A SUITABLE APPARATUS FOR CARRYING OUT THE METHOD | Morguet, Peter |
| 10/931,482 | 9/1/2004 | CMOS-PROCESS COMPATIBLE HIGH-DC SURFACE COATING FOR CAPACITIVE DETECTION AND STIMULATION OF BIOLOGICAL TISSUES | Birkenmaier, Tamara; Eversmann, Bjorn-Oliver; Fromherz, Peter; Gabl, Reinhard; Jenker, Marin; Schreiter, Matthias; Thewes, Roland; Wersing, Wolfram |
| 10/934,302 | 9/3/2004 | APPARATUS FOR PROVIDING A JITTERED CLOCK SIGNAL AND APPARATUS FOR PROVIDING A RANDOM BIT | Luzzi, Raimondo |
| 10/937,745 | 9/8/2004 | CAPACITOR ASSEMBLY AND METHOD FOR PRODUCING THE SAME | Schrenk, Michael |
| 10/946,579 | 9/20/2004 | VOLTAGE REGULATOR ARRANGEMENT | Fleischmann, Peter; Nebel, Gerhard; Schlaffer, Andreas; Weder, Uwe |
| 10/948,627 | 9/23/2004 | RANDOM NUMBER GENERATOR AND METHOD FOR GENERATING A RANDOM NUMBER | Sturm, Thomas; Stromberg, Guido; Brederlow, Ralf; Weber, Werner |
| 10/948,637 | 9/23/2004 | METHOD FOR PRODUCING AN SOI FIELD EFFECT TRANSISTOR AND CORRESPONDING FIELD EFFECT TRANSISTOR | Gottsche, Ralf; Pacha, Christian; Schulz, Thomas; Steinhogl, Werner |
| 10/952,240 | 9/28/2004 | INTEGRATED CIRCUIT HAVING RADIATION SENSOR ARRANGEMENT | Laackmann, Peter |
| 10/952,472 | 9/27/2004 | LINE-CONDUCTED INTERFACE ARRANGEMENT AND A METHOD FOR OPERATING A LINE-CONDUCTED INTERFACE ARRANGEMENT BETWEEN A HOST AND A DEVICE | Ruping, Stefan; Cinkler, Kalman |

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| 10/958,182 | 10/4/2004 | CIRCUIT WITH A BUS WITH SEVERAL RECEIVERS | Klug, Franz; Kunemund, Thomas; Sonnekalb, Steffen |
| 10/958,644 | 10/5/2004 | PSEUDORANDOM NUMBER GENERATOR FOR A STREAM CIPHER | Dirscherl, Gerd; Gammel, Berndt; Gottfert, Rainer |
| 10/964,560 | 10/12/2004 | EVENT DETECTION - APPARATUS AND METHOD FOR MEASURING THE ACTIVITY OF NEURAL NETWORKS | Eversmann, Bjorn-Oliver; Jenkner, Martin; Paulus, Christian; Thewes, Roland |
| 10/965,663 | 10/14/2004 | CIRCUIT ARRANGEMENT AND METHOD FOR PRODUCING A DUAL-RAIL SIGNAL | Kunemund, Thomas; Sedlak, Holger |
| 10/966,847 | 10/15/2004 | METHOD FOR REPLACING CONTENTS OF A DATA STORAGE UNIT | Gammel, Berndt |
| 10/967,090 | 10/15/2004 | DEVICE BASED ON PARTIALLY OXIDIZED POROUS SILICON AND METHOD FOR ITS PRODUCTION | Dertinger, Stephan; Fritz, Michaela; Fuchs, Karin; Haneder, Thomas; Lehmann, Volker; Martin, Alfred; Marz, Reinhard |
| 10/968,846 | 10/18/2004 | SUBSTRATE AND METHOD FOR PRODUCING A SUBSTRATE | Hofmann, Franz; Luyken, R. Johannes; Stadele, Martin; Rosner, Wolfgang; Specht, Michael |
| 10/968,847 | 10/19/2004 | DEVICE AND METHOD FOR DETECTING BIOCHEMICAL REACTIONS AND/OR BINDINGS | Dertinger, Stephan; Fritz, Michaela; Fuchs, Karin; Haneder, Thomas; Hanke, Hans-Christian; Martin, Alfred; Marz, Reinhard |
| 10/970,363 | 10/21/2004 | CIRCUIT ARRANGEMENT FOR VOLTAGE REGULATION | Schlaffer, Andreas |
| 10/974,542 | 10/27/2004 | COMPONENT | Hubner, Holger |
| 10/977,561 | 10/28/2004 | APPARATUS AND METHOD FOR CALCULATING A RESULT OF A MODULAR MULTIPLICATION | Fischer, Wieland; Sedlak, Holger; Seifert, Jean-Pierre |
| 10/978,241 | 10/29/2004 | APPARATUS AND METHOD FOR GENERATING A RANDOM NUMBER | Meintrup, David; Stromberg, Guido; Sturm, Thomas; Stohr, Annelie |
| 10/978,242 | 10/29/2004 | INTEGRATED CIRCUIT HAVING A MIXER CIRCUIT | Langenberg, Jorg; Liebermann, Thomas; Simburger, Werner; Tiebout, Marc; Wohlmuth, Hans-Dieter |
| 10/979,787 | 11/1/2004 | GARMENT WITH INTEGRATED ELECTRONIC COMPONENTS | Schweizer, Thomas; Janke, Marcus |
| 10/980,983 | 11/3/2004 | CONTACT-CONNECTION OF NANOTUBES | Unger, Eugen; Dusberg, Georg Stefan; Kreupl, Franz Martin; Graham, Andrew; Liebau, Maik |
| 10/988,422 | 11/12/2004 | CIRCUIT AND METHOD FOR PERFORMING A CALCULATION | Bock, Holger |
| 10/989,600 | 11/16/2004 | CLOCK REGULATION APPARATUS AND CIRCUIT ARRANGEMENT | Mahrla, Peter |
| 10/991,345 | 11/9/2004 | NON-VOLATILE FLASH SEMICONDUCTOR MEMORY AND FABRICATION METHOD | Hofmann, Franz; Landgraf, Erhard; |

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| | | | Rosner, Wolfgang; Specht, Michael; Staedele, Martin |
| 10/992,467 | 11/18/2004 | AUTHORIZATION MEANS SECURITY MODULE TERMINAL SYSTEM | Hewel, Karl-Harald |
| 10/992,627 | 11/17/2004 | APPARATUS FOR HOUSING A MICROMECHANICAL STRUCTURE AND METHOD FOR PRODUCING THE SAME | Franosch, Martin; Meckes, Andreas; Nessler, Winfried; Oppermann, Klaus-Gunter |
| 10/995,960 | 11/22/2004 | LOGIC BASIC CELL AND LOGIC BASIC CELL ARRANGEMENT | Gliese, Jorge; Sauermann, Mirko |
| 10/996,620 | 11/19/2004 | LOGIC CIRCUIT ARRANGEMENT | Gliese, Jorg; Scheppler, Michael |
| 10/999,809 | 11/29/2004 | METHOD, COMPUTER PROGRAM, APPARATUS AND SYSTEM FOR THE SELECTIVE COMMUNICATION OF DATA SETS | Mergenthaler, Max Egon, Dr.; Preuninger, Jurgen |
| 10/999,810 | 11/29/2004 | METHOD FOR THE PRODUCTION OF A MEMORY CELL, MEMORY CELL AND MEMORY CELL ARRANGEMENT | Hofmann, Franz; Luyken, Richard Johannes; Specht, Michael |
| 11/001,781 | 12/1/2004 | MODULE FOR CONTACTLESS CHIP CARDS OR IDENTIFICATION SYSTEMS | Puschner, Frank; Muller-Hipper, Andreas; Karl, Andreas |
| 11/006,519 | 12/6/2004 | PROCESSOR AND METHOD FOR A SIMULTANEOUS EXECUTION OF A CALCULATION AND A COPYING PROCESS | Elbe, Astrid; Fischer, Wieland; Janssen, Norbert; Sedlak, Holger; Seifert, Jean-Pierre |
| 11/007,591 | 12/8/2004 | PROTECTION OF CHIPS AGAINST ATTACKS | Laackmann, Peter |
| 11/007,592 | 12/8/2004 | AUTHENTICITY TAG AND METHOD FOR OPERATING AN AUTHENTICITY TAG | Laackmann, Peter |
| 11/007,650 | 12/7/2004 | LOGIC BASIC CELL, LOGIC BASIC CELL ARRANGMENT AND LOGIC DEVICE | Gliese, Jorge; Schonauer, Tim |
| 11/008,585 | 12/9/2004 | DEVICE AND METHOD FOR GENERATING RANDOM NUMBERS USING A PSEUDO RANDOM NUMBER GENERATOR | Gammel, Berndt; Goettfert, Rainer; Sedlak, Holger |
| 11/009,402 | 12/10/2004 | MICROPROCESSOR ARRANGEMENT AND METHOD FOR OPERATING A MICROPROCESSOR ARRANGEMENT | Janke, Marcus |
| 11/016,374 | 12/17/2004 | SYSTEM FOR INTERCHANGING DATA BETWEEN AT LEAST TWO CONTACTLESS DATA STORAGE MEDIA | Berger, Dominik; Melchior, Marc |
| 11/018,404 | 12/20/2004 | LOGIC CIRCUIT | Bock, Holger |
| 11/019,328 | 12/14/2004 | DEVICE FOR OPTICAL SIGNAL TRANSMISSION, METHOD FOR OPTICAL SIGNAL TRANSMISSION AND OPTICAL MODULATOR | Averbeck, Robert; Stegmuller, Bernhard |
| 11/020,631 | 12/22/2004 | DEVICE AND METHOD FOR PERFORMING A CRYPTOALGORITHM | Rueping, Stephan |
| 11/022,278 | 12/23/2004 | TRANSMISSION DEVICE | Kunemund, Thomas |
| 11/035,765 | 1/13/2005 | SENSOR ARRANGEMENT WITH IMPROVED SPATIAL AND TEMPORAL RESOLUTION | Eversmann, Bjorn-Oliver; Jenkner, Martin; Paulus, Christian; Stromberg, Guido; Sturm, Thomas; Stohr, Annelie |
| 11/041,084 | 1/21/2005 | SEMICONDUCTOR MEMORY DEVICE AND CIRCUIT ARRANGEMENT | Dortu, Jean-Marc |
| 11/045,002 | 1/26/2005 | INTEGRATED CIRCUIT ARRANGEMENT | Janke, Marcus; Laackmann, Peter |
| 11/053,096 | 2/7/2005 | VARIABLE LENGTH INSTRUCTION PIPELINE | Norden, Erik K.; Arnold, Roger D.; Ober, Robert E.; Hastie, Neil S. |
| 11/055,570 | 2/9/2005 | CONTACTLESS DATA STORAGE MEDIUM HAVING A SIGNAL INDICATOR | Ruping, Stefan; Klement, Rupert |
| 11/062,317 | 2/18/2005 | METHOD FOR OPERATING A PERIPHERAL DEVICE ON A BUS SYSTEM | Schneckenburger, |

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| | | OF A COMPUTER SYSTEM | Christian |
| 11/062,318 | 2/18/2005 | PROCESS FOR PRODUCING A METAL STRUCTURE IN FOAM FORM, A METAL FOAM, AND AN ARRANGEMENT HAVING A CARRIER SUBSTRATE AND A METAL FOAM | Karl, Andreas; Muller-Hipper, Andreas; Simmerlein-Erlbacher, Ewald |
| 11/062,319 | 2/18/2005 | READING DEVICE AND METHOD FOR CHECKING THE FUNCTIONALITY OF A READING DEVICE OF THIS TYPE | Berger, Dominik |
| 11/064,525 | 2/22/2005 | METHOD AND CIRCUIT FOR CONTROLLING THE INPUT SIGNAL FOR A CONTACTLESS TRANSPONDER | Alihodzic, Admir; Kargl, Walter; Missoni, Albert |
| 11/064,528 | 2/22/2005 | METHOD FOR SMOOTHING AREAS IN STRUCTURES BY UTILIZING THE SURFACE TENSION | Domke, Wolf-Dieter; Schwarzl, Siegfried |
| 11/067,309 | 2/25/2005 | DECRYPTION CIRCUIT, ENCRYPTION CIRCUIT, LOGIC CELL, AND METHOD OF PERFORMING A DUAL-RAIL LOGIC OPERATION IN SINGLE-RAIL LOGIC ENVIRONMENT | Kunemund, Thomas |
| 11/070,843 | 2/24/2005 | DATA PROCESSING APPARATUS HAVING PROGRAM COUNTER SENSOR | Gail, Markus; Otterstedt, Jan |
| 11/073,017 | 3/4/2005 | HIGH-DENSITY NROM-FINFET | Hofmann, Franz; Landgraf, Erhard; Luyken, Richard Johannes; Rosner, Wolfgang; Specht, Michael |
| 11/073,018 | 3/4/2005 | KEY BIT STREAM GENERATION | Gottfert, Rainer; Gammel, Berndt; Cinkler, Kalman; Ruping, Stefan |
| 11/080,998 | 3/14/2005 | CALCULATING UNIT | Janssen, Norbert; Roemer, Tanja; Sedlak, Holger |
| 11/083,334 | 3/17/2005 | RECTIFIER CIRCUIT | Alihodzic, Admir |
| 11/084,216 | 3/18/2005 | METHOD AND CIRCUIT ARRANGEMENT FOR DEMODULATING A DIGITAL AMPLITUDE-MODULATED RADIO SIGNAL | Alihodzi, Admir |
| 11/084,725 | 3/18/2005 | RANDOM NUMBER GENERATOR AND METHOD FOR GENERATING RANDOM NUMBERS | Gammel, Berndt; Goettfert, Rainer |
| 11/086,801 | 3/22/2005 | METHOD AND SYSTEM FOR COMPUTER-AIDED SPEECH SYNTHESIS | Küstner, Michael; Schnell, Markus |
| 11/088,228 | 3/23/2005 | CONTACTLESS DATA CARRIER WITH CURRENT DEMODULATION | Missoni, Albert |
| 11/090,914 | 3/24/2005 | APPARATUS AND METHOD FOR CONVERTING, AND ADDER CIRCUIT | Elbe, Astrid; Janssen, Norbert; Sedlak, Holger |
| 11/090,972 | 3/24/2005 | CONTACTLESS DATA STORAGE MEDIUM AND METHOD FOR OPERATING CONTACTLESS DATA STORAGE MEDIUM | Berger, Dominik |
| 11/093,276 | 3/28/2005 | PROCESSOR FOR PROCESSING A PROGRAM | Dirscherl, Gerd; Gammel, Berndt; Smola, Michael |
| 11/094,841 | 3/30/2005 | TRANSMISSION INTERFACE | Ruping, Stefan; Sustek, Laurent |
| 11/097,968 | 3/31/2005 | METHOD AND SYSTEM FOR CONTACTLESS DATA TRANSMISSION | Kargl, Walter |
| 11/101,171 | 4/7/2005 | METHOD AND APPARATUS FOR CORRELATING CODES IN CDMA SYSTEMS | Medlock, Joel D. |
| 11/101,365 | 4/7/2005 | METHOD AND APPARATUS FOR CONFIGURING INFORMATION | Cummings, Mark R. |
| 11/104,293 | 4/11/2005 | CIRCUIT ARRANGEMENT | Missoni, Albert |
| 11/107,986 | 4/15/2005 | CIRCUIT ARRANGEMENT FOR VOLTAGE REGULATION | Baglin, Thomas Jean Ludovic; Nebel, Gerhard |
| 11/111,096 | 4/19/2005 | METHOD AND DEVICE FOR DETERMINING A RESULT | Sonnekalb, Steffen Marc; Wenzel, Andreas |
| 11/112,739 | 4/22/2005 | METHOD FOR PRODUCING A BGA CHIP MODULE AND BGA CHIP MODULE | Puschner, Frank; Schindler, Wolfgang; Tutsch, Gunter |

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| 11/115,985 | 4/27/2005 | DATA PROCESSING APPARATUS AND METHOD FOR OPERATING A DUAL RAIL CIRCUIT | Kunemund, Thomas |
| 11/116,139 | 4/27/2005 | INTEGRATED CIRCUIT ARRAY | Graham, Andrew; Hofmann, Franz; Honlein, Wolfgang; Kretz, Johannes; Kreupl, Franz; Landgraf, Erhard; Luyken, Richard Johannes; Rosner, Wolfgang; Schulz, Thomas; Specht, Michael |
| 11/119,426 | 4/28/2005 | COMMUNICATIONS SYSTEM | Dirscherl, Gerd; Winteler, Till |
| 11/119,531 | 4/29/2005 | MEMORY CELL, MEMORY CELL ARRANGEMENT, PATTERNING ARRANGEMENT, AND METHOD FOR FABRICATING A MEMORY CELL | Graham, Andrew; Hofmann, Franz; Honlein, Wolfgang; Kretz, Johannes; Kreupl, Franz; Landgraf, Erhard; Luyken, Richard Johannes; Rosner, Wolfgang; Schulz, Thomas; Specht, Michael |
| 11/120,659 | 5/2/2005 | DEVICE AND METHOD FOR GENERATING A PSEUDORANDOM SEQUENCE OF NUMBERS | Goettfert, Rainer; Meyer, Bernd; Dirscherl, Gerd; Seifert, Jean-Pierre |
| 11/120,660 | 5/2/2005 | APPARATUS AND METHOD FOR STRUCTURE EXPOSURE OF A PHOTOREACTIVE LAYER | Stommer, Ralph |
| 11/121,689 | 5/3/2005 | SMART CARD | Knoll, Bernhard Theo |
| 11/125,819 | 5/9/2005 | MEMORY ARRANGEMENT | Dirscherl, Gerd; Peters, Christian; Sedlak, Holger |
| 11/129,068 | 5/13/2005 | DEVICE AND METHOD FOR SUPPLYING A DATA TRANSFER UNIT WITH ENERGY | Baglin, Thomas Jean Ludovic; Missoni, Albert |
| 11/130,555 | 5/16/2005 | CONTROLLING OUT OF ORDER EXECUTION PIPELINES ISSUE TAGGING | Hastie, Neil S. |
| 11/131,452 | 5/16/2005 | LOGIC BASIC CELL | Gliese, Jorg |
| 11/132,145 | 5/20/2005 | CONTROLLER HAVING DECODING MEANS | Klug, Franz; Kniffler, Oliver; Sonnekalb, Steffen; Wenzel, Andreas |
| 11/134,026 | 5/19/2005 | INTERFACE CIRCUIT | Beit-Grogger, Andre; Koroschetz, Helmut |
| 11/134,612 | 5/20/2005 | CONTROLLER AND METHOD FOR PROCESSING INSTRUCTIONS | Klug, Franz; Kniffler, Oliver; Sonnekalb, Steffen; Wenzel, Andreas |
| 11/136,611 | 5/23/2005 | METHOD FOR DETECTING THE RELATIVE MOVEMENT OF A FINGER IN RELATION TO A SENSOR SURFACE | Lorch, Henning; Morguet, Peter |
| 11/137,698 | 5/24/2005 | METHOD AND SYSTEM FOR PRODUCING CONDUCTIVE PATTERNS ON A SUBSTRATE | Karl, Andreas; Muller-Hipper, Andreas; Puschner, Frank; Simmerlein-Erlbacher, Ewald |
| 11/138,909 | 5/25/2005 | METHOD AND SYSTEM FOR CHECKING COMPLETENESS IN A PACKAGE | Berger, Dominik; Muller, Dietmar; Weitlaner, Matthias |
| 11/142,007 | 5/31/2005 | COMMUNICATION SYSTEM | Schmidt, Holger; Hans, Martin; |

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| 11/144,373 | 6/3/2005 | ANALOG DC COMPENSATION | Beckmann, Mark |
| 11/147,401 | 6/6/2005 | COMMUNICATION SYSTEM | Hjelm, Mikael |
| 11/147,414 | 6/6/2005 | CONFIGURABLE LOGIC CIRCUIT | Junfeng, Rong; Eckert, Michael; Wuschke, Martin; Schmidt, Holger |
| 11/147,418 | 6/6/2005 | RADIO COMMUNICATION RECEIVER | Otterstedt, Jan |
| 11/149,450 | 6/8/2005 | FREQUENCY CONVERTER CIRCUIT | Rudberg, Mikael |
| 11/152,420 | 6/13/2005 | METHOD AND SYSTEM FOR CONTACTLESS DATA TRANSMISSION, AND USE OF THE METHOD | Knapp, Herbert; Wurzer, Martin |
| 11/153,914 | 6/14/2005 | METHOD AND SYSTEM FOR OPERATING A CACHE MEMORY | Berger, Dominik |
| 11/155,321 | 6/16/2005 | CIRCUIT FOR GENERATING A SUPPLY VOLTAGE | Dirscherl, Gerd; Gammel, Berndt M.; Smola, Michael |
| 11/155,322 | 6/16/2005 | TRANSISTOR ARRANGEMENT | Alihodzic, Admir; Baglin, Thomas Jean Ludovic |
| 11/157,495 | 6/20/2005 | RADIO-INTERROGABLE DATA STORAGE MEDIUM | Brederlow, Ralf |
| 11/157,496 | 6/20/2005 | FIN FIELD EFFECT TRANSISTOR MEMORY CELL | Laackmann, Peter |
| 11/165,823 | 6/23/2005 | PARALLEL DATA BUS | Dreeskornfeld, Lars; Hartwich, Jessica; Hofmann, Franz; Kretz, Johannes; Specht, Michael |
| 11/165,834 | 6/23/2005 | MODULAR MULTIPLICATION WITH PARALLEL CALCULATION OF THE LOOK-AHEAD PARAMETERS | Smola, Michael; Gammel, Berndt M.; Dirscherl, Gerd |
| 11/166,645 | 6/23/2005 | APPARATUS AND METHOD FOR CALCULATING A MULTIPLICATION | Fischer, Wieland; Sedlak, Holger; Seifert, Jean-Pierre |
| 11/166,657 | 6/23/2005 | METHOD AND DEVICE FOR EXTRACTING A CLOCK FREQUENCY UNDERLYING A DATA STREAM | Fischer, Wieland; Seifert, Jean-Pierre; Sedlak, Holger |
| 11/166,685 | 6/23/2005 | DIGITALLY CONTROLLABLE OSCILLATOR | Greco, Patrizio; Steinschaden, Andreas; Thaller, Edwin; Zessar, Gernot |
| 11/169,157 | 6/27/2005 | APPARATUS FOR DETECTING CONNECTION OF A PERIPHERAL UNIT TO A HOST SYSTEM | Greco, Patrizia; Steinschaden, Andreas; Thaller, Edwin; Zessar, Gernot |
| 11/175,912 | 7/5/2005 | LAYER ARRANGEMENT AND PROCESS FOR PRODUCING A LAYER ARRANGEMENT | Cinkler, Kalman; Winteler, Till |
| 11/176,587 | 7/6/2005 | SELECTION CIRCUIT | Ilicali, Gurkan; Landgraf, Erhard; Roesner, Wolfgang; Hofmann, Franz |
| 11/176,941 | 7/6/2005 | DATA STORAGE MEDIUM HAVING A TEST MODE | Baglin, Thomas Jean Ludovic |
| 11/177,235 | 7/7/2005 | PLANAR DUAL-GATE TRANSISTOR AND METHOD FOR FABRICATING A PLANAR DUAL-GATE TRANSISTOR | Papadopoulos, Constantin; Gammel, Berndt M. |
| 11/178,251 | 7/8/2005 | PROCESS FOR PRODUCING A LAYER ARRANGEMENT, AND LAYER ARRANGEMENT FOR USE AS A DUAL-GATE FIELD-EFFECT TRANSISTOR | Ilicali, Gurkan; Luyken, Richard Johannes; Rosner, Wolfgang |
| | | | Luyken, Richard Johannese; Ilicali, Gurkan; Johannese, Richard; Rosner, Wolfgang |

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| 11/179,356 | 7/11/2005 | APPARATUS AND METHOD FOR MULTIMODE DIGITAL DESPREADER FOR CDMA SIGNAL PROCESSING | Subramanian, Ravi |
| 11/180,039 | 7/11/2005 | SEMICONDUCTOR CHIP STACK | Hubner, Holger |
| 11/181,032 | 7/12/2005 | VOLTAGE SUPPLY CIRCUIT AND METHOD FOR GENERATING A SUPPLY VOLTAGE | Baglin, Thomas Jean Ludovic; Missoni, Albert; Nebel, Gerhard |
| 11/181,033 | 7/12/2005 | INTEGRATED CIRCUIT | Kunemund, Thomas |
| 11/184,566 | 7/18/2005 | METHOD AND APPARATUS FOR DETECTING LUMINESCENCE LIGHT FROM A POROUS SUPPORT STRUCTURE | Dertinger, Stephan; Kluehr, Marco |
| 11/187,039 | 7/20/2005 | SECURE MASKING IN THE PRESENCE OF GLITCHES | Gammel, Berndt; Fischer, Wieland |
| 11/193,038 | 7/29/2005 | DEVICE AND METHOD FOR CALCULATING ENCRYPTED DATA FROM UNENCRYPTED DATA OR UNENCRYPTED | Bock, Holger; Aigner, Manfred |
| 11/193,851 | 7/29/2005 | A WIRELESS SPREAD SPECTRUM COMMUNICATION PLATFORM USING DYNAMICALLY RECONFIGURABLE LOGIC | Subramanian, Ravi; Jha, Uma; Medlock, Joel D. |
| 11/195,332 | 8/1/2005 | LAYER ARRANGEMENT | Schindler, Gunther; Pamler, Werner |
| 11/195,350 | 8/1/2005 | MODULAR EXPONENTIATION WITH RANDOMIZED EXPONENT | Fischer, Wieland |
| 11/197,776 | 8/3/2005 | DEVICE AND METHOD FOR GENERATING A SEQUENCE OF NUMBERS | Gottfert, Rainer; Gammel, Berndt |
| 11/198,692 | 8/5/2005 | A WIRELESS SPREAD SPECTRUM COMMUNICATION PLATFORM USING DYNAMICALLY RECONFIGURABLE LOGIC | Subramanian, Ravi; Jha, Uma; Medlock, Joel D. |
| 11/200,854 | 8/9/2005 | METHOD AND APPARATUS TO SUPPORT MULTISTANDARD, MULTISERVICE BASE-STATIONS FOR WIRELESS VOICE AND DATA NETWORKS | Subramanian, Ravi; Rieken, Keith; Woodthrope, Christopher C. |
| 11/202,944 | 8/12/2005 | INTEGRATED CIRCUIT | Gammel, Berndt; Kunemund, Thomas |
| 11/207,599 | 8/19/2005 | TEXTILE FABRIC STRUCTURE | Glaser, Rupert Hermann Josef; Jung, Stefan; Lauterbach, Christ |
| 11/211,167 | 8/23/2005 | CHIP CARD MODULE | Puschner, Frank; Stampka, Peter; Muller-Hipper, Andreas; Schindler, Wolfgang |
| 11/211,168 | 8/23/2005 | INTEGRATED ELECTRONIC COMPONENT | Seidel, Robert; Kreupl, Franz; Graham, Andrew |
| 11/213,341 | 8/25/2005 | SEMICONDUCTOR CHIP ARRANGEMENT | Eigner, Markus; Gruber, Wolfgang; Roth, Manfred; Ruping, Stefan |
| 11/213,663 | 8/25/2005 | CIRCUIT ARRANGEMENT | Ruping, Stefan |
| 11/215,350 | 8/29/2005 | COMMUNICATION SYSTEM | Hans, Martin; Luft, Achim |
| 11/216,457 | 8/30/2005 | CHIP MODULE | Muller-Hipper, Andreas; Puschner, Frank |
| 11/218,315 | 8/31/2005 | ENHANCED MULTI-MEDIA-CARD AND CARD-READER FOR EMERGENCE RELATED APPLICATIONS IN MOBILE | Kodim, Walter |
| 11/221,382 | 9/6/2005 | METHOD FOR OPERATING A DATA STORAGE MEDIUM | Ruping, Stefan |
| 11/221,383 | 9/6/2005 | METHOD FOR OPERATING A MICROPROCESSOR | Gammel, Berndt; Sonnekalb, Steffen |
| 11/222,504 | 9/7/2005 | APPARATUS AND METHOD FOR RECOGNIZING A FAILURE OF A CRYPTOGRAPHIC UNIT | Gammel, Berndt; Goessel, Michael; Goettfert, Rainer |
| 11/227,963 | 9/14/2005 | MEDIUM ACCESS CONTROL UNIT | Choi, Hyung-Nam; Eckert, Michael |

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|---|---|---|---|
| 11/227,964 | 9/14/2005 | CHIP CARD | Kargl, Walter |
| 11/233,419 | 9/16/2005 | APPARATUS AND METHOD FOR GENERATING A FIBONACCI VECTOR FIELD FROM A GALOIS FIELD | Medlock, Joel D. |
| 11/236,311 | 9/26/2005 | METHOD FOR PRODUCING CHIP STACKS | Hubner, Holger |
| 11/238,645 | 9/28/2005 | DATA LINK LAYER PROTOCOL UNIT | Eckert, Michael; Choi, Hyung-Nam; Wuschke, Martin |
| 11/240,815 | 9/30/2005 | ARRANGEMENT AND METHOD FOR CONTROLLING COMMUNICATION OF DATA BETWEEN PROCESSORS | Esch, Michael; Rost, Astrid |
| 11/240,816 | 9/30/2005 | MEMORY ARRANGEMENT | Scheiblhofer, Dietmar; Jernej, Jurgen |
| 11/240,817 | 9/30/2005 | VOLTAGE SUPPLY DEVICE AND METHOD FOR OPERATING SUCH A VOLTAGE SUPPLY DEVICE | Scheiblhofer, Dietmar |
| 11/246,015 | 10/6/2005 | RANDOM NUMBER GENERATOR AND METHOD FOR GENERATING RANDOM | Luzzi, Raimondo; Bucci, Marco |
| 11/251,623 | 10/14/2005 | SHELF ARRANGEMENT AND SHELF UNIT | Glaser, Rupert; Lauterbach, Christl; Sturm, Thomas |
| 11/252,361 | 10/13/2005 | APPARATUS AND METHOD FOR REQUESTING OR ALLOCATING A PUSH-TO-TALK RIGHT TO TALK AND/OR FOR REQUESTING OR COMMUNICATING QUEUING INFORMATION | Schwagmann, Norbert; Schmidt, Andreas; Schmidt, Holger |
| 11/261,453 | 10/27/2005 | DEVICE AND METHOD FOR THE COMPUTER-AIDED MANAGEMENT OF A TELECOMMUNICATION CONFERENCE | Schmidt, Holger; Schwagmann, Norbert; Schmidt, Andreas |
| 11/264,294 | 10/31/2005 | INTEGRATED CIRCUIT ARRANGEMENT | Benetik, Thomas; Hodel, Uwe; Kienmayer, Christoph; Streibl, Martin; Tiebout, Marc |
| 11/264,679 | 10/31/2005 | METHOD FOR AUTOMATICALLY SETTING UP AND/OR CONTROLLING A TELECOMMUNICATION CONFERENCE | Schwagmann, Norbert; Schmidt, Andreas; Schmidt, Holger; Benthin, Marcus, Dr. |
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## Signature of Assignee of Record

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| Henriette Post<br>Patent Administrator | Date<br>04/19/2006 | Susanne Girstenbreu<br>Patent Administrator | Date<br>04/19/2006 |